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## TRANSMITTAL FORM

(to be used for all correspondence after initial filing)

Total Number of Pages in This Submission

Application Number	09/760,242
Filing Date	01/12/2001
First Named Inventor	Robert J. Davidson
Art Unit	2554
Examiner Name	James R. Sheleheda
Total Number of Pages in This Submission	10002343-1 (SEAG 77938)

### ENCLOSURES (Check all that apply)

<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> After Allowance Communication to TC
<input type="checkbox"/> Fee Attached	<input type="checkbox"/> Licensing-related Papers	<input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences
<input type="checkbox"/> Amendment/Reply	<input type="checkbox"/> Petition	<input type="checkbox"/> Appeal Communication to TC (Appeal Notice, Brief, Reply Brief)
<input type="checkbox"/> After Final	<input type="checkbox"/> Petition to Convert to a Provisional Application	<input type="checkbox"/> Proprietary Information
<input type="checkbox"/> Affidavits/declaration(s)	<input checked="" type="checkbox"/> Power of Attorney, Revocation	<input type="checkbox"/> Status Letter
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Remarks		

### SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm Name	Pietragallo, Bosick & Gordon LLP		
Signature			
Printed name	Benjamin T. Queen, II		
Date	05/23/2006	Reg. No.	41,260

### CERTIFICATE OF TRANSMISSION/MAILING

I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below:

Signature			
Typed or printed name	Benjamin T. Queen, II	Date	05/23/2006

This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT

MAY 26 2006



Inventor(s):	Robert J. Davidson		
Serial No.:	09/760,242	Examiner:	James R. Sheleheda
Filed:	01/12/2001	Group Art Unit:	2554
Title:	Personal Movie Storage Module		
Attorney Docket No.:	10002343-1 (SEAG 77938)		

**POWER OF ATTORNEY BY ASSIGNEE OF ENTIRE INTEREST  
(REVOCATION OF PRIOR POWERS)**

As assignee of record of the entire interest of the above identified application,  
**REVOCATION OF PRIOR POWERS OF ATTORNEY**

all powers of attorney previously given are hereby revoked and

**NEW POWER OF ATTORNEY**

the following practitioners/patent agents are hereby appointed to prosecute and transact all business in the Patent and Trademark Office connected therewith.

Practitioners at Customer Number  
OR  
 Practitioner(s) named below:

35810

Name	Registration Number

**PLEASE CHANGE ATTORNEY'S/AGENT'S ADDRESS IN APPLICATION TO:**

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**STATEMENT UNDER 37 CFR 3.73(b)**

Seagate Technology LLC states that it is the Assignee of Entire Interest in the patent application/patent identified above by virtue of an Assignment from the inventor(s) of the patent application/patent identified above. A copy of the Assignment is attached, or was recorded in the Patent and Trademark Office at Reel \_\_\_, Frame \_\_\_. The undersigned (whose title is supplied below) is empowered to sign this statement on behalf of the Assignee.

Respectfully submitted,

SEAGATE TECHNOLOGY LLC  
(Assignee of Entire Interest)

5/18/06  
Date

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## HEWLETT-PACKARD/SEAGATE PATENT ASSIGNMENT

This Patent Assignment (this "*Patent Assignment*") is entered into as of February 15, 2006 ("Effective Date"), is executed by and between Hewlett-Packard Company, a Delaware corporation having a business address at 3000 Hanover Street, Palo Alto, CA 94304 ("HPC") and Hewlett-Packard Development Company, L.P., a Texas limited partnership having a business address at 20555 S.H. 249, Houston, TX 77070 ("HPDC", HPC and HPDC, collectively, "HP") on the one hand, and Seagate Technology LLC, a Delaware limited liability company ("Seagate"), on the other.

### 1. PATENT ASSIGNMENT

In exchange for good and valuable consideration, the receipt and sufficiency of which are hereby acknowledged, HP hereby assigns to Seagate all rights, title and interest to: (a) the following U.S. patents and patent applications:

#### (i) Issued Patents

- Pat. No. 6,310,794, issued 30-Oct-01, entitled "Upgradable Storage System"
- Pat. No. 6,404,647, issued 11-Jun-02, entitled "Solid-State Mass Memory Storage Device"
- Pat. No. 6,615,123, issued 02-Sep-03, entitled "Personality Module For Configuring A Vehicle"
- Pat. No. 6,629,193, issued 30-Sep-03, entitled "Solid-State Information Storage Device (As Amended)"
- Pat. No. 6,635,940, issued 21-Oct-03, entitled "Micro-Electromechanical Actuator And Methods Of Use"
- Pat. No. 6,661,334, issued 09-Dec-03, entitled "Methods And Apparatus For Verifying The Installation Of Components In A System"
- Pat. No. 6,738,336, issued 18-May-04, entitled "Data Storage Device"
- Pat. No. 6,751,686, issued 15-Jun-04, entitled "Automated Transfer Of A Data Unit Comprising A Plurality Of Fundamental Data Units Between A Host Device And A Storage Medium"
- Pat. No. 6,862,206, issued 01-Mar-05, entitled "Memory Module Hybridizing An Atomic Resolution (ARS) Memory And A Magnetic Memory"
- Pat. No. 6,822,933, issued 23-Nov-04, entitled "High Density Data Storage Module"
- Pat. No. 6,897,083, issued 24-May-05, entitled "Micro-Electromechanical Actuator And Methods Of Use And Fabrication"
- Pat. No. 6,922,353, issued 26-Jul-05, entitled "Memory For Storing Information"

#### (ii) Pending Applications

- Ser. No. 09/759,867, filed 12-Jan-01, entitled "Portable Information Storage Module For Information Shopping"
- Ser. No. 09/760,242, filed 12-Jan-01, entitled "Personal Movie Storage Module"
- Ser. No. 10/180,232, filed 26-Jun-02, entitled "Systems And Methods For Storing Information To Allow Users To Manage Files"
- Ser. No. 10/200,970, filed 24-Jul-02, entitled "Memory Card File Recovery System"
- Ser. No. 10/243,263, filed 13-Sep-02, entitled "System For Quickly Transferring Data"
- Ser. No. 10/295,651, filed 15-Nov-02, entitled "Transferring Data In Selectable Transfer Modes"
- Ser. No. 10/396,000, filed 24-Mar-03, entitled "Expandable Capacity Storage Device"
- Ser. No. 10/423,103, filed 25-Apr-03, entitled "Information Transfer Protocol"

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- Ser. No. 10/607,315, filed 26-Jun-03, entitled "Methods And Apparatus For Verifying The Installation Of Components In A System"
- Ser. No. 10/643,350, filed 19-Aug-03, entitled "Solid-State Information Storage Device"
- Ser. No. 10/646,178, filed 22-Aug-03, entitled "Data Storage Device"
- Ser. No. 10/653,756, filed 03-Sep-03, entitled "Memory Card With A Modular Component"
- Ser. No. 10/678,295, filed 03-Oct-03, entitled "Memory Module"
- Ser. No. 10/727,402, filed 04-Dec-03, entitled "Systems And Methods For Translating Voltage Levels Of Digital Signals"
- Ser. No. 10/817,208, filed 02-Apr-04, entitled "Storage Device Having A Probe To Cooperate With A Storage Medium To Provide A Variable Resistance"
- Ser. No. 10/827,370, filed 20-Apr-04, entitled "Contact Probe Storage FET Sensor And Write Heater Arrangements"
- Ser. No. 10/848,841, filed 19-May-04, entitled "Storage Device Having First And Second Magnetic Elements That Interact Magnetically To Indicate A Storage State"
- Ser. No. 10/884,839, filed 02-Jul-04, entitled "Integrated Metallic Contact Probe Storage Device"
- Ser. No. 11/081,038, filed 15-Mar-05, entitled "Apparatus And Method For Reading Bit Values Using Microprobe On A Cantilever"

(the "Assigned Patent(s)"); (b) the right to file and prosecute any continuations, continuations-in-part, divisionals, reissues, re-examinations, substitutions and/or foreign equivalents of any of the foregoing; and (c) the right to sue for any past infringement of any of the foregoing, and to collect damages therefor. Seagate takes the Assigned Patent(s) subject to all outstanding licenses and grants of any kind made by HP prior to the Effective Date.

## 2. ENTIRE AGREEMENT

This Patent Assignment, together with any agreements specifically contemplated or delivered in connection herewith, constitutes the entire agreement between the parties regarding the subject matter hereof.

IN WITNESS WHEREOF, the parties have agreed as of the date first written above.

Agreed to:

SEAGATE TECHNOLOGY LLC

By: William L. Hudson

Name: William L Hudson

Title: Executive Vice President and General Counsel

Agreed to:

HEWLETT-PACKARD COMPANY

By: Joseph A. Beyers

Name: Joe Beyers

Title: Vice President, Intellectual Property

Agreed to:

HEWLETT-PACKARD DEVELOPMENT COMPANY L.P.

By: HPQ Holdings, LLC, its General Partner

By: Shane Robison

Name: Shane Robison

Title: Manager